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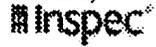
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BEA Tuxedo ATMI Architecture

... and management of data buffers that works in all environments supported by ... buffers of another message buffer type) to package the application data. ...

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... Initial buffer fullness clarification at buffering period SEI message ...

HRD information concerning the number of CPBs, their bitrates and buffer sizes ...

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... The N-BASE uses separate macros for memory management (typically used for local data storage) and buffer management (typically used for sending messages ...

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... IBTA v1.1 Compatible Design; Supports Millions of **Queue Pairs**; InfiniRISCTM III Embedded ... Support for Eight Data VLs plus the **VL15 Management Lane** ...

www.mellanox.com/products/hca.html - 29k - May 6, 2005 -

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Managing System Performance

... such as those between **pairs** of applications, and simplex data streams such ... always assigned to the high-priority **queue**; all other **management** traffic ...

[www.cisco.com/univercd/cc/td/doc/product/](http://www.cisco.com/univercd/cc/td/doc/product/software/ios113ed/113ed_cr/fun_c/fcprt4/fcperfrm.htm)

[software/ios113ed/113ed_cr/fun_c/fcprt4/fcperfrm.htm](http://www.cisco.com/univercd/cc/td/doc/product/software/ios113ed/113ed_cr/fun_c/fcprt4/fcperfrm.htm) - 53k - [Cached](#) - [Similar pages](#)

BEA Tuxedo ATMI Core Components

... Fielded **buffers** contain attribute-value **pairs** in fields, ... have a rich array of functions for the definition and **management** of FML fields and **buffers**. ...

e-docs.bea.com/tuxedo/tux81/overview/atmi.htm - 41k - [Cached](#) - [Similar pages](#)

O'Reilly Network: An Introduction to the InfiniBand Architecture

... copying of data between the user level **buffers** and the NIC memory. ... **queue pairs** can implemented in hardware, thereby off-loading most of the work ...

www.oreillynet.com/pub/a/network/2002/02/04/windows.html?page=last - 42k -

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Topspin host channel adapter product specification

... Lanes Support for eight data VLs plus the **VL15 management lane** Queues Support for up to 128 K **queue pairs** ... Large on-chip InfiniBand port **buffers** ...

www.topspin.qassociates.co.uk/products-host-channel-adapter-specs.htm - 24k -

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Software Architecture Specification (SAS)

... Verb requires each vendor to perform all processing of Subnet **Management** Packets ... **queue pairs** and completion queues and registered the memory **buffers** ...

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[PDF] Software Architecture Specification (SAS)

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... domains, set up **queue pairs** and completion queues, and establish ... vendors may allocate **buffers** for Completion **Queue** in user mode and program hardware ...

infiniband.sourceforge.net/archive/HCA_DDK.pdf - [Similar pages](#)

Monitoring Buffer-Pool Activity

... availability as well as information on the **buffers** in each LRU **queue**. ... 8 buffer LRU **queue pairs** # f/m length % of pair total 0 f 3 37.5% 8 1 m 5 ...
 publib.boulder.ibm.com/infocenter/ids9help/topic/com.ibm.admin.doc/admin325.htm - 9k -
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 ... failed device only affects its own attached hardware link and the **queue pairs** ... is a region of memory to be used for **buffers**, a key that must match on ...
 www.infinibandta.org/newsroom/whitepapers/sky_InfiniBand_Arch_090602.pdf -
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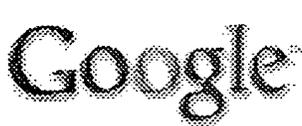
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... Communication Management. The CM Agent assists in connecting **queue pairs** ... Although users may not post **buffers** to a MAD Service to receive incoming ...

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www.qmgroupp.com

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... the **queue pairs** at the two parties before they can communi- ... can **pre-post** a number of receiver **buffers** for each con- nection. ...

nowlab.cis.ohio-state.edu/projects/mpi-iba/publication/liu_cac04.pdf - [Similar pages](#)

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... for pre-posting of application receive **buffers**. We dem- ... access to databases of key/data **pairs**. It is linked into the ...

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[PDF] Exploiting Direct-Access Networking in Network Attached Storage ...

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... to **pre-post** application **buffers** with the NIC, enabling direct data transfers between the ... by enabling sender-based **management** of receiver **buffers**. ...

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... used in compound bindings where pipeline stages perform **pre/post** processing ... interface reference **management**, threads, **buffers** etc., and ii) binding ...

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... server **pairs** in which the client made repeated request/ reply calls on the ... interface reference **management**, threads, **buffers** etc., and ii) binding ...

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... NIC interaction is required on each I/O to **pre-post**. application receive **buffers**. ... access to databases of key/data **pairs**. It is linked into the ...

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... to processing local messages will simplify message queue management ... We extend functions on actor descriptions to configuration-actor pairs by first ...

osl.cs.uiuc.edu/docs/phd-venkatas98/PHDVenkatasubramanian.pdf - [Similar pages](#)

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... be used to prevent extremely large articles creating huge memory buffers. ... queue files include offset/length pairs to allow dnewslink to access the ...

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